#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	David A. Zarneke
Attorney Docket No	MI22-1572
Title: Methods of Forming Board-On-Chip Packages	

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

# **CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8**

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9318 on the date shown below:

- 1. Certificate of Facsimile Transmission
- 2. Fee Deficiency Statement
- 2. Supplemental Information Disclosure Statement
- 3. Form PTO-1449
- 4. Three (3) cited references

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Dated: July 7, 2003

Muriel G. Dunnigan

Telephone No. (509) 624-4276

NUMBER OF PAGES IN FACSIMILE: 37

### FEE DEFICIENCY

The Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: 7-7-03

D. Brent Kenady; Reg. No. 40,045 Telephone No. (509) 624-4276

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No 09/756,971
Filing Date January 9, 2001
Inventor
Assignee Micron Technology, Inc.
Group Art Unit
Examiner David A. Zarneke
Attorney's Docket No MI22-1572
Title: Methods of Forming Board-On-Chip Packages

#### SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References —See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449. No admission is made regarding whether the submitted references are prior art.

Citation of the referenced art is respectfully requested.

The Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: <u>7-7-</u>03

D. Brent Kenady Reg. No. 40,045

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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT Salman Akram								
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